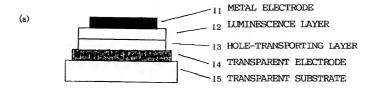
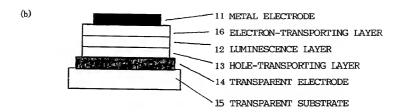
FIG. 1





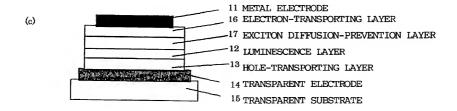


FIG. 2

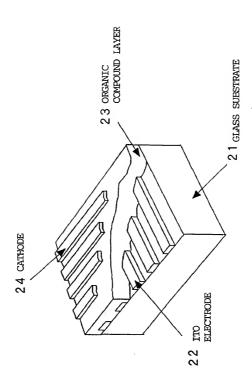


FIG. 3

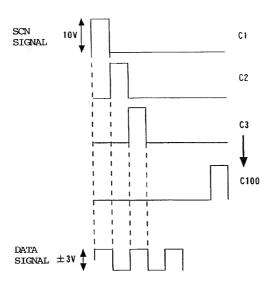
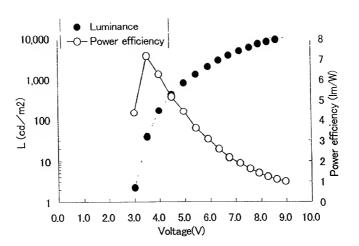
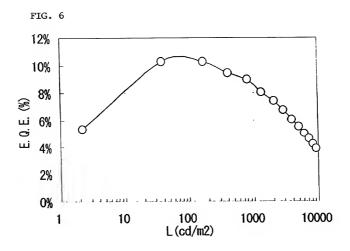


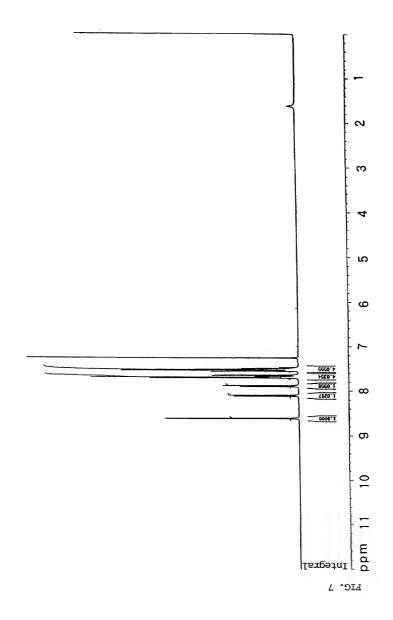
FIG. 4

DATA SIGNAL DRIVER									
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SCAN SIGNAL DRIVER	$_{\rm G2}$			1					C R
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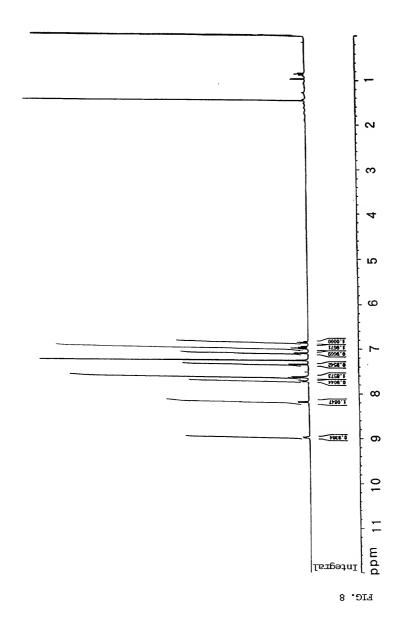




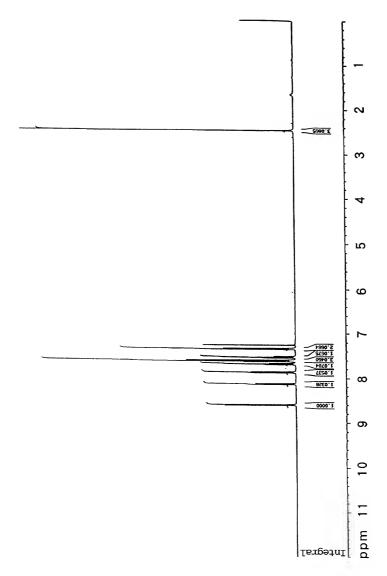




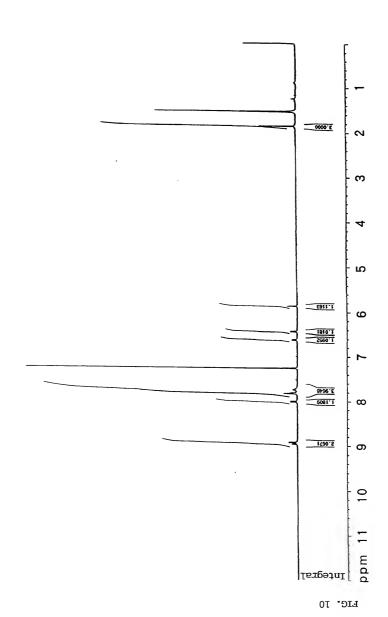
11/9



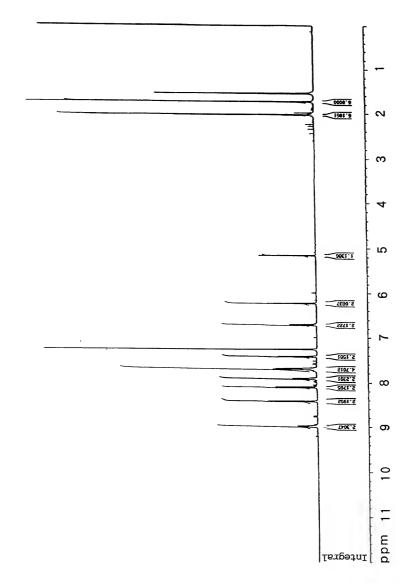
I I / 9



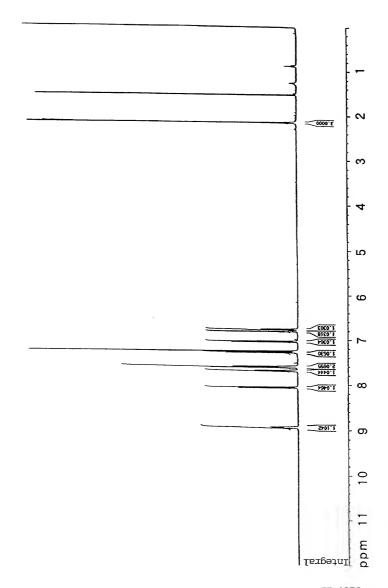
EIG. 9



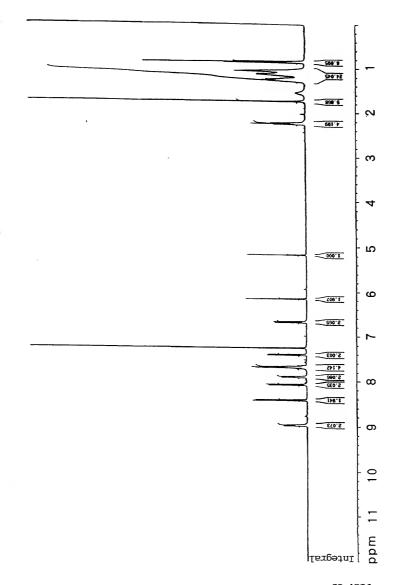
1 1 / 8



EIG. 11



EIG. 12



EIC. 13